

212

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 09/912,745

Filed: 07/24/2001

Inventor(s):

Richard A. Zartorski

Title: Bus Bridge Circuit
Including Audio Logic and
an Addressable Register for
Storing an Address Bit Used
When the Audio Logic
Accesses Digital Data, and
Method for Initializing a
Chip Set Including the Bus
Bridge Circuit

Examiner: Lefkowitz, Sumati
Group/Art Unit: 2112
Atty. Dkt. No: 5681-46700

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

B. Noël Kivlin

Printed Name

Signature

Date

RESPONSE TO OFFICE ACTION OF
April 22, 2004

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of April 22, 2004, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.